

# Programmable Optical Interferometers for Matrix Multiplication

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Programmable photonic circuits offer new possibilities for computing providing means for fast and energy-efficient matrix-vector multiplication. Fastest and most convenient realizations are based on linear integrated photonics. We will discuss several approaches to the design of integrated matrix multiplication cores with an emphasis on coherent architectures based on programmable interferometers. We will present several architectures approaching the limits of lowest interferometer depth and resilience to fabrication and programming errors. Current technology limits the achievable scale of integrated optical circuits posing challenges and trade-offs for their adoption as multiplication cores in hybrid opto-electronical tensor processors. In the case of coherent interferometers programming these devices to implement specified matrix operations may also be challenging. We will raise the question of programmability for integrated interferometers and the required computational overheads.